



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No. : 09/895,997
Applicant : Shinya Hasegawa
Filed : June 28, 2001
TC/A.U. : 2115
Examiner : Cao, Chun
Title : SEMICONDUCTOR INTEGRATED CIRCUIT AND
FUNCTIONAL BLOCK INCLUDING SYNCHRONIZING
CIRCUIT FOR DETERMINING OPERATION TIMING
(AS AMENDED)

Docket No. : 2102475-991130
Customer No. : 26379

RECEIVED

NOV 24 2004

Technology Center 2100

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Certificate of Mailing

I hereby certify that this correspondence is being deposited with the
United States Postal Service with sufficient postage as FIRST CLASS
MAIL in an envelope addressed to: Commissioner for Patents,
P.O. Box 1450, Alexandria, VA 22313-1450, on:

November 17, 2004


Rosa A. Caviedes

RESPONSE TO OFFICE ACTION OF AUGUST 17, 2004

Sir:

In response to the Office Action of August 17, 2004, please amend the above identified application as follows:

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper.

Remarks/Arguments begin on page 6 of this paper.